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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/041,957	01/07/2002	Steven Teig	SPLX.P0044	5414
48947	7590 08/11/2005		EXAMINER	
	R, JOHANSEN, AND A	TAT, BINH C		
	URY PARK EAST SUITE CITY, CA 90067	E 1050	ART UNIT	PAPER NUMBER
	,		2825	
			DATE MAILED: 08/11/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

- ¹.		AZ -
	Application No.	Applicant(s)
	10/041,957	TEIG ET AL.
Office Action Summary	Examiner	Art Unit
	Binh C. Tat	2825
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with th	e correspondence address
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply if NO period for reply is specified above, the maximum statutory period w.  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	86(a). In no event, however, may a reply be within the statutory minimum of thirty (30) rill apply and will expire SIX (6) MONTHS fi cause the application to become ABANDC	e timely filed  days will be considered timely.  om the mailing date of this communication.  NED (35 U.S.C. § 133).
Status		
<ul> <li>1) Responsive to communication(s) filed on 19 Ma</li> <li>2a) This action is FINAL. 2b) This</li> <li>3) Since this application is in condition for allowant closed in accordance with the practice under E</li> </ul>	action is non-final. nce except for formal matters,	
Disposition of Claims		
4)	vn from consideration.	
Application Papers		
9)☐ The specification is objected to by the Examiner 10)☒ The drawing(s) filed on <u>07 January 2002</u> is/are: Applicant may not request that any objection to the o Replacement drawing sheet(s) including the correcti 11)☐ The oath or declaration is objected to by the Examiner	a)⊠ accepted or b)⊡ object drawing(s) be held in abeyance. on is required if the drawing(s) is	See 37 CFR 1.85(a). objected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) ☐ Acknowledgment is made of a claim for foreign     a) ☐ All b) ☐ Some * c) ☐ None of:     1. ☐ Certified copies of the priority documents     2. ☐ Certified copies of the priority documents     3. ☐ Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list of the certified copies.	s have been received. s have been received in Applic ity documents have been rece (PCT Rule 17.2(a)).	ation No ived in this National Stage
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  Paper No(s)/Mail Date	4)  Interview Summ. Paper No(s)/Mai 5)  Notice of Informa 6)  Other:	

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#### **DETAILED ACTION**

This office action is in response to application 10/041957 filed on 02/07/02.
 Claims 27-47 remain pending in the application.

## Response to Arguments

2. Applicant's arguments with respect to claims 27-47 have been considered but are persuasive in view of the new ground's of rejection.

#### Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 27-47 are rejected under 35 U.S.C. 102(e) as being anticipated by Teig et al., "U. S. patent 6826737".

4. As to claims 27, 46, and 47 Das et al. a method teach of pre-computing routes for nets, the method comprising: a) prior to performing a routing operation, defining a set of partitioning lines for partitioning (see fig 1, fig 7, and fig 8, fig 12), during the routing operation, a region of an integrated circuit ("IC") layout into a plurality of sub-regions (see fig 1, fig 7, and fig 8, fig 12 col 9 lines 62 to col 11, lines 35); b) for a set of potential sub-regions, identifying a set of at least two routes that traverse the potential set of sub-regions, wherein at least one of the routes has at least one diagonal edge (see col 13 lines 1-16, especially 1-3, 14-16; col 14. lines 26-31); storing

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the identified routes, wherein said stored routes are for use during the routing operation (see col 14 lines 26-40).

- 5. As to claim 28 Das et al. teach wherein a plurality of paths exist between the sub-regions defined by the set of partitioning lines, wherein a plurality of the paths are diagonal paths, wherein at least one of the routes traverses some of the diagonal (see col 13 lines 1-16, especially 1-3, 14-16; col 14. lines 26-31 and background).
- 6. As to claims 29 Das et al. teach wherein identifying the routes comprises identifying the paths between the sub-regions used by each route (see col 13 lines 1-16, especially 1-3, 14-16; col 14. lines 26-40).
- 7. As to claim 30, Das et al. teach wherein a plurality of the paths are Manhattan paths, wherein at least one of the routes traverses some of the Manhattan paths (see col 13 lines 1-16, especially 1-3, 14-16; col 14. lines 26-40 and summary).
- 8. As to claim 31-33 Das et al. wherein a plurality of edges exist between the sub-regions defined by the set of partitioning lines, wherein a plurality of the edges between the sub-regions are diagonal edges, wherein at least one of the routes intersects at least one of the diagonal edges (see col 13 lines 1-16, especially 1-3, 14-16; col 14. lines 26-40).
- 9. As to claim 34-36 Das et al. further comprising: a) for each particular set of potential sub-regions from a group of potential-sub-region sets and wherein the group of sets includes all possible sets of sub-regions including sets with zero or one sub-region, wherein the identified sets of routes for sets of sub-regions with zero or one sub-region are empty, identifying a set of routes that traverse the particular set of potential sub-regions, wherein some of the routes have

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diagonal edges (see col 13 lines 1-16, especially 1-3, 14-16; col 14. lines 26-40 and background and summary); and b) storing the identified routes (see col 13 lines 1-16, especially 1-3, 14-16; col 14. lines 26-40 and summary).

- As to claim 37 and 42 Das et al. teach for a roister that uses a set of partitioning lines to partition an integrated circuit ("IC") layout region into a plurality of sub-regions, wherein a plurality of routing paths exist between the sub-regions, a method of pre-computing routes for connecting said sub-regions, the method comprising: for each particular combination of two or more sub-regions, identifying at least one route for connecting the particular combination of said sub-regions, said identifying performed before a routing operation (see fig 1, fig 7, and fig 8, fig 12 col 9 lines 62 to col 11, lines 35); identifying the routing paths used by each identified route, wherein some of the identified routing paths are diagonal (see col 13 lines 1-16, especially 1-3, 14-16; col 14. lines 26-31); and storing the identified routing paths for each identified routes in a storage structure wherein said stored routing paths are for use during the routing operation (see col 14 lines 26-40).
- 11. As to claim 38, 41 and 43 Das et al. teach wherein some of the routing paths are horizontal (see col 13 lines 1-16, especially 1-3, 14-16; col 14. lines 26-40).
- 12. As to claim 39 and 44 Das et al. teach wherein some of the routing paths are Manhattan (see col 2 lines 63 to col 4 lines 41).
- 13. As to claim 40 and 45 Das et al. teach wherein the Manhattan routing paths are defined with respect to a first grid, and wherein the diagonal routing paths are defined with respect to a second grid (see col 13 lines 1-16, especially 1-3, 14-16; col 14. lines 26-40 and background).

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### Conclusion

Any inquiry concerning this communication or earlier communications from the 14. examiner should be directed to Binh C. Tat whose telephone number is (703) 305-4855. The examiner can normally be reached on 7:30 - 4:00 (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mathew Smith can be reached on (703) 308-1323. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 305-3431 for regular communications and (703) 305-3431 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-1782.

Binh Tat Art Unit 2825 August 6, 2005

Umendo THUAN DD Primary examiner. 08/00/2005